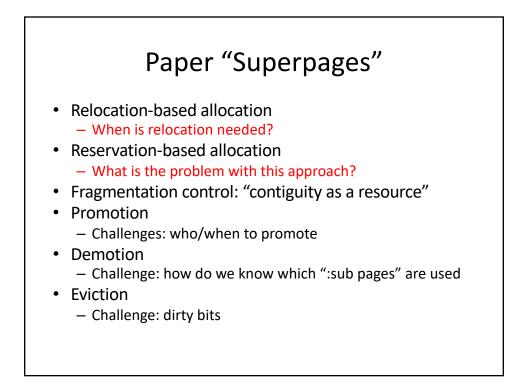
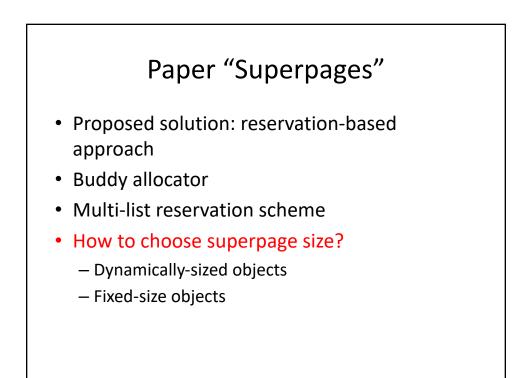
Graduate Operating Systems (Memory Management)

Fall 2020

Paper "Superpages" Small pages vs. large pages TLB coverage (how large should it be?) Hardware-imposed constraints Page sizes supported by hardware Contiguous in physical and virtual address space Starting address must be multiple of its size

 TLB uses single set of reference/dirty/protection bits for page





Paper "Superpages"

- Preempting reserved (unused) frames
- Coalescing of available memory regions
 - Contiguity-aware page replacement (active/inactive/cache lists)
- Incremental promotions
 - Cascading promotions possible
- Speculative demotions
 - E.g., due to eviction; to next-smaller size
 - Probabilistic demotions

