

FALL 2017 CDA 3103 Fundamentals of Computer Systems Tentative Schedule

Tuesday		Thursday	
<u>22-Aug</u>	Introduction, Number Systems (2)	<u>24-Aug</u>	Binary & Hexadecimal Numbers (2)
<u>29-Aug</u>	Two's Complement Integers (2)	<u>31-Aug</u>	IEEE Real Representations (2)
<u>5-Sep</u>	7-bit ASCII, Strings (2)	<u>7-Sep</u>	No Class - Hurricane Irma
<u>12-Sep</u>	No Class - Hurricane Irma	<u>14-Sep</u>	No Class - Hurricane Irma
<u>19-Sep</u>	Bit-Manipulation, Review (2)	<u>21-Sep</u>	Transistors, Logic Gates (3)
<u>26-Sep</u>	Test 1: Data Representation (2)	<u>28-Sep</u>	2-Level AND-OR Circuits, PLA (3)
<u>3-Oct</u>	Decoder, Multiplexer (3)	<u>5-Oct</u>	Adders, Combinational Circuits (3)
<u>10-Oct</u>	RS & D Latches, Registers (3)	<u>12-Oct</u>	Random Access Memory, Data Path (3)
<u>17-Oct</u>	FSMs, Data Path, Review (3)	<u>19-Oct</u>	The von Neumann Model (4)
<u>24-Oct</u>	Test 2: Digital Logic Fundamentals (3)	<u>26-Oct</u>	The Instruction Cycle (4)
<u>31-Oct</u>	The LC-3 ISA, Operate Instructions (5)	<u>2-Nov</u>	LC-3 Data Movement Instructions (5)
<u>7-Nov</u>	LC-3 BR Instruction, Programming (5)	<u>9-Nov</u>	LC-3 Machine Language Programming (5)
<u>14-Nov</u>	LC-3 Machine Language Programming (5)	<u>16-Nov</u>	LC-3 Assembly Language (7)
<u>21-Nov</u>	Two-Pass Assembly Process (7)	<u>23-Nov</u>	Thanksgiving Day Holiday
<u>28-Nov</u>	LC-3 Control Instructions JSR(R), TRAP (9)	<u>30-Nov</u>	Polling & Memory Mapped I/O (8)
<u>5-Dec</u>	The Stack, Interrupt-Driven I/O (8, 10)	<u>7-Dec</u>	LC-3 RTI Instruction (10)
<u>12-Dec</u>	Test 3: Computer Systems (4, 5, 7, 8, 9, 10)	<u>14-Dec</u>	Test 3: Computer Systems (4, 5, 7, 8, 9, 10)
	U01 (T/R 9:30 am) - 9:45 am		U03 (T/R 6:25 pm) - 5:00 pm
	U04 (T/R 7:50 pm) - 7:15 pm		

6-Nov Last Day to DDrop or Withdraw