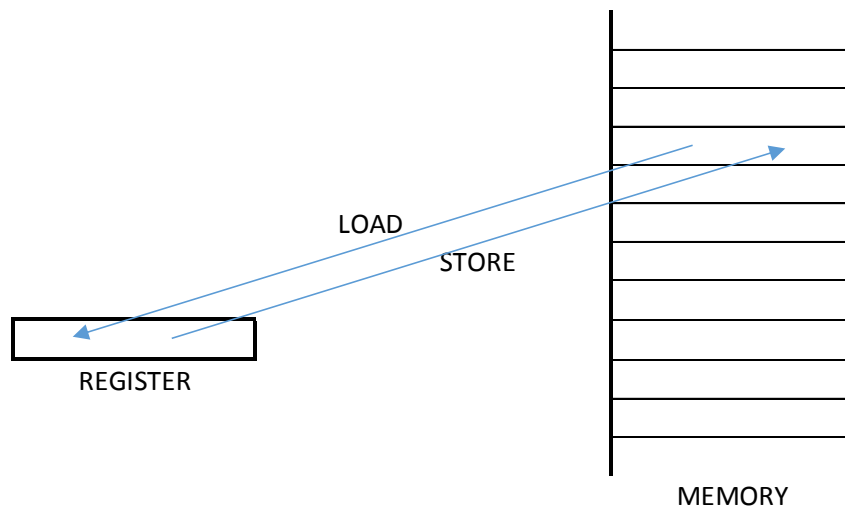
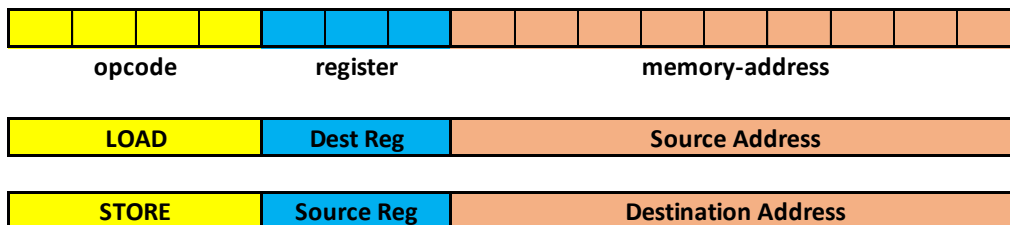


LC-3 Data-Movement Instructions



LOAD Register Memory-Address

STORE Register Memory-Address



Memory-Addressing Modes

PC-Relative Effective Address: 9-bit offset added with the address from PC

Indirect Indirect Address: 9-bit offset added with the address from PC
Effective Address: Content of the Indirect Address

Base Register Effective Address: 6-bit offset added with the base address from a general-purpose register (the base register)