

## School of Computer Science

**Course Title:** Fundamentals of Computer Systems

**Date:** 10/30/03

**Course Number:** COP-3402

**Number of Credits:** 3

|  |   |
|--|---|
| <b>Subject Area:</b> Computer Systems  | <b>Subject Area Coordinator:</b><br>Masoud Sadjadi<br><b>email:</b> sadjadi@cis.fiu.edu |
| <b>Catalog Description:</b><br>Overview of computer systems organization. Data representation. Machine and assembly language programming.      |   |
| <b>Textbook:</b> Introduction to Computing Systems, 2 <sup>nd</sup> Edition<br>Yale N. Patt, Sanjay J. Patel<br>McGraw-Hill (ISBN: 0072467509) |   |
| <b>References:</b>   |   |
| <b>Prerequisites Courses:</b> COP-2210 or equivalent   |   |
| <b>Corequisites Courses:</b> MAD 2104  |   |

Type: Required

Prerequisites Topics:

- High level programming language constructs
- Function call/return
- Parameters of a function(method)

Course Outcomes:

1. Master the representations of numeric and character data
2. Master the implementation of some basic combinational circuits, registers and memories
3. Be familiar with the data path of a simple von Neumann architecture and its relation to the instruction execution cycle
4. Master simple machine and assembly language programming
5. Master the implementation of high-level language constructs in lower levels: selection, iteration, function call/return

**School of Computer Science**  
**COP-3402**  
**Fundamentals of Computer Systems**

**Outline**

| <b>Topic</b>  | <b>Number of<br/>Lecture Hours</b> | <b>Outcome</b> |
|---|------------------------------------|----------------|
| <ul style="list-style-type: none"> <li>• Machine level representation               <ul style="list-style-type: none"> <li>○ Numeric data representation</li> <li>○ Signed &amp; unsigned representation</li> <li>○ Fixed- and floating-point systems</li> <li>○ Integer arithmetic</li> <li>○ Boolean operations</li> </ul> </li> </ul>  | 8                                  | 1              |
| <ul style="list-style-type: none"> <li>• Digital logic               <ul style="list-style-type: none"> <li>○ Fundamental building blocks<br/>(logic gates, combinational circuits)</li> <li>○ Von Neumann model</li> <li>○ Instruction execution cycle</li> </ul> </li> </ul>  | 8                                  | 2,3            |
| <ul style="list-style-type: none"> <li>• Assembly level machine organization               <ul style="list-style-type: none"> <li>○ Instruction sets and types</li> <li>○ Assembly language programming</li> <li>○ Addressing modes</li> <li>○ Subroutines and system routines</li> <li>○ I/O and interrupts</li> <li>○ Bit level manipulation</li> <li>○ Assembly process and linking</li> </ul> </li> </ul> | 14                                 | 4,5            |
| <ul style="list-style-type: none"> <li>• Introduction to architecture               <ul style="list-style-type: none"> <li>○ Hierarchy of virtual machines</li> <li>○ Interpretation and translation</li> <li>○ Simple machine architecture</li> </ul> </li> </ul>  | 8                                  | 3              |

**School of Computer Science  
COP-3402  
Fundamentals of Computer Systems**

**Course Outcomes Emphasized in Laboratory Projects / Assignments**

|   | <b>Outcome</b>                               | <b>Number of Weeks</b> |
|---|--|------------------------|
| 1 | Data representation<br>Outcome: 1            | 1                      |
| 2 | Digital circuit design<br>Outcomes: 2        | 2                      |
| 3 | Architecture concepts<br>Outcomes: 3         | 2                      |
| 4 | Machine language programming<br>Outcomes: 4  | 2                      |
| 5 | Assembly language programming<br>Outcomes: 5 | 2                      |
| 6 | Assembly language programming<br>Outcomes: 5 | 2                      |

**Oral and Written Communication:**

No significant coverage

**Social and Ethical Implications of Computing Topics**

No significant coverage

**School of Computer Science  
COP-3402  
Fundamentals of Computer Systems**

**Approximate number of credit hours devoted to fundamental CS topics**

| <b>Topic</b>                                   | <b>Core Hours</b> | <b>Advanced Hours</b> |
|--|-------------------|-----------------------|
| <b>Algorithms:</b>                             |                   |                       |
| <b>Software Design:</b>                        |                   |                       |
| <b>Computer Organization and Architecture:</b> | <b>2.0</b>        |                       |
| <b>Data Structures:</b>                        |                   |                       |
| <b>Concepts of Programming Languages:</b>      | <b>1.0</b>        |                       |

**Theoretical Contents**

| <b>Topic</b>    | <b>Class time</b> |
|-----------------|-------------------|
| Boolean algebra | 1.0               |

**Problem Analysis Experiences**

|   |
|---|
| Implementation of high level programming language constructs in low level languages |
|---|

**Solution Design Experiences**

1. 

|                        |
|------------------------|
| Digital circuit design |
|------------------------|
2. 

|                               |
|-------------------------------|
| Assembly language programming |
|-------------------------------|

**School of Computer Science**  
**COP-3402**  
**Fundamentals of Computer Systems**

**The Coverage of Knowledge Units within Computer Science Body of Knowledge<sup>1</sup>**

| <b>Knowledge Unit</b>      | <b>Topic</b>   | <b>Lecture Hours</b> |
|----------------------------|--|----------------------|
| <a href="#"><u>PL2</u></a> | Virtual machine, hierarchy of virtual machines, intermediate languages   | 8                    |
| <a href="#"><u>AR1</u></a> | History of computer architecture, fundamental logic circuits, gate delays  | 8                    |
| <a href="#"><u>AR2</u></a> | Bits, bytes, and words, numeric data representation, fixed- and floating-point systems, signed and twos-complement representations, nonnumeric data (character codes, graphical data), representation of records and arrays  | 8                    |
| <a href="#"><u>AR3</u></a> | von Neumann machine, control unit; instruction fetch, decode, and execution, instruction sets and types (data manipulation, control, I/O), assembly/machine language programming, instruction formats, addressing modes, subroutine call and return mechanisms, I/O and interrupts | 14                   |

---

<sup>1</sup>See <http://www.computer.org/education/cc2001/final/chapter05.htm> for a description of Computer Science Knowledge units